

# SAMXON BRAND ALUMINUM ELECTROLYTIC CAPACITORS

# PRODUCT SPECIFICATION 規格書

CUSTOMER: DATE:

(客戶): (日期):2017-07-10

CATEGORY (品名) : ALUMINUM ELECTROLYTIC CAPACITORS

DESCRIPTION (型号) : GT 16V1500μF(φ10x20)

VERSION (版本) : 01

Customer P/N :

SUPPLIER :

SUPPLIER								
PREPARED (拟定)	CHECKED (审核)							
李婷	刘渭清							

CUST	CUSTOMER								
APPROVAL (批准)	SIGNATURE (签名)								

# ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

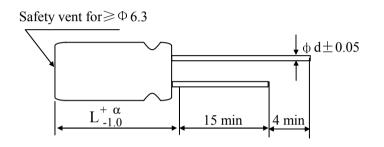
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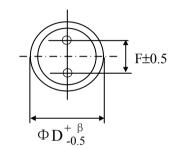
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### Table 1 Product Dimensions and Characteristics





α	L<20 : α=1.5; L≥20 : α=2.0
β	$\Phi D < 20 : \beta = 0.5; \Phi D \ge 20 : \beta = 1.0$

N o.	SAMXON Part No.	WV (Vdc)	Cap. (μF)	Cap. tolerance	Temp. range( $^{\circ}$ C)	tanδ (120Hz, 20℃)	Leakage Current (uA,2min)	Max Ripple Current at 105℃ 100kHz (mA rms)	Impedance at 20°C 100kHz	Load lifetime (Hrs)		ension mm)	фd	Sleev
1	EGT158M1CG20RR**P	16	1500	-20%~+20%	-40~105	0.16	240	1400	(Ωmax) 0.046	7000	10X20	5.0	0.6	PET

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# ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

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# 1. Application

This specification applies to polar Aluminum electrolytic capacitor (foil type) used in electronic equipment. Designed capacitor's quality meets IEC60384.

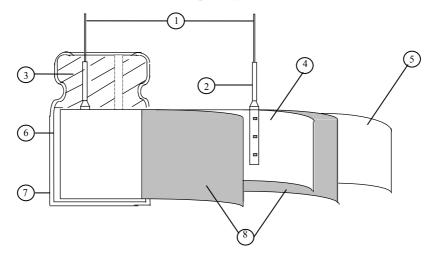
2	t Numb		7 5 N	Ī	8 9 1 H VOLTAGE	[	10 1 <sup>o</sup>	1		C	1516 S A SAMXON PRODUCT LINE M	17 P SLEEV
\	Con (MED)	Codo	T-1(9/)	Code	Naltara (MAX)	Codo	0000	Cima	Facture	2 a da		
Series ESM EKF ESS	Cap(MFD) 0.1	Code 104	Tolerance (%)	J	Voltage (W.V.) 2 2.5	0D 0E	Case Diameter 3	(a) Code	Feature (	RR	SAMXON Product L For internal use only (The product lines	
EKS EGS EKM	0.22	224	±10	к	6.3	0G 0J	4 5	1 C D	Ammo Tap	ing	we have H,A,B,C,D, E,M or 0,1,2,3,4,5,9	
EKG EOM	0.33	334		H	10	0K 1A	8 10	ΙFΙ	2.0mm Pitch	тт		
EZM EZS EGF	0.47	474	±15	<u> </u>	12.5 16	1B 1C	13	J	2.5mm Pitch	TU		
ESF EGT EGK	1,	105	±20	м	20	1D 1E	13.5 14 14.5 16	V 4 A K	3.5mm Pitch	τv	Sleeve Material	Code
EGE EGD EGC	2.2	225	±30	N	30 32	1I 13	14.5 16 16.5 18 18.5	A K 7 L 8	5.0mm Pitch	тс	PET	Р
ERS ERF	3.3	335	-40 0	w	35 40	1V 1G	20 22	M N	Lead Cut & F	Form		
ERL ERR ERT	4.7	475	-20 0		42	1M 1H	18 18.5 20 22 25 30 34 35	P W	СВ-Туре	СВ		
ERE ERD	10	106		$\vdash$	57 63	1L 1J	35 40	Q R	CE-Type	CE		
ERH EBD ERA	22	226	-20 +10	С	71	18	40 42 45 51	Q R 4 6 S T	HE-Type	HE		
ERB ERC	33	336	-20 +40	×	75 80	1T 1K	63.5 76 80	U 8	KD-Type	KD		
EFA ENP ENH	47	476	-20 +50	s	85 90	1R 19	90 100 Len.(mm)	X	FD-Type	FD		
ERW ERY ELP	100	107	-10 0	В	100 120	2A 2O	4.5 5	45 05	EH-Type	EH		
EAP EQP EDP	220	227	-10 +20	V	125 150	2B 2Z	7.7	54 07 77	PCB Term	nial		
ETP EHP	330	337	-10 +30	Q	160 180	2C 2P	10.2 11 11.5	11 1A		sw		
EUP EKP EEP	470	477	-10 +50	T	200 215	2D 22	12 12.5 13	12 1B	Snap-in	sx		
EFP ESP EVP	2200	228	-5 +10	E	220 230	2N 23	13.5 20 25 29.5	13 1C 20 25 2J		sz		
EVP EGP EWR EWU	22000	229	-5 +15	F	250 275	2E 2T	30	1301	Lug	SG		
EWX	33000	339		G	300 310	2I 2R	31.5 35 35.5	3A 35 3E		05		
EWF EWS EWH	47000	479	-5 +20 0	$\vdash$	315 330	2F 2U	50 80	50 80		O6		
EWL EWB	100000	10T	+20	R	350 360	2V 2X	100 105 110 120	1L 1K 1M	Screw	T5		
VSS VNS VKS	150000	15T	+30	0	375 385	2Q 2Y		1N		Т6		
VKM VRL	220000	22T	+50		400	2G	150 155	1R 1E		D5		
VNH VZS VRF	330000	33T	+5 +15	z	420 450	2M 2W	160 165 170	11S 11F 11T		D6		
	1000000	10M	+5 +20	D	500 550	2H 25	180 190	1U 1V				
	1500000	15M	+10 +50	Y	630	26 2J	215 210	2A 2M				
	2200000	22M	+10 +30	н			240 250	1P 1Q 1R 1S 1F 1T 1U 1V 2A 2M 2N 2Q 2R 2S 2S				
	3300000	33M					260 270	2S 2T				

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#### 3. Construction

Single ended type to be produced to fix the terminals to anode and cathode foil, and wind together with paper, and then wound element to be impregnated with electrolyte will be enclosed in an aluminum case. Finally sealed up tightly with end seal rubber, then finished by putting on the vinyl sleeve.



	Component	Material
1	Lead line	Tinned CP wire (Pb Free)
2	Terminal	Aluminum wire
3	Sealing Material	Rubber
4	Al-Foil (+)	Formed aluminum foil
5	Al-Foil (-)	Etched aluminum foil or formed aluminum foil
6	Case	Aluminum case
7	Sleeve	PET
8	Separator	Electrolyte paper

#### 4. Characteristics

#### Standard atmospheric conditions

Unless otherwise specified, the standard range of atmospheric conditions for making measurements and tests are as follows:

Ambient temperature :15°C to 35°C Relative humidity : 45% to 85% Air Pressure : 86kPa to 106kPa

If there is any doubt about the results, measurement shall be made within the following conditions:

Ambient temperature :  $20^{\circ}\text{C} \pm 2^{\circ}\text{C}$ Relative humidity : 60% to 70%Air Pressure : 86kPa to 106kPa

#### Operating temperature range

The ambient temperature range at which the capacitor can be operated continuously at rated voltage See table 1 temperature range.

As to the detailed information, please refer to table 2.

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	ITEM				PERFO	RMANC	CE				
	Rated voltage										
	(WV)	WV (V.DC)	6.3	10	16	25	35	50	63	100	
4.1		SV (V.DC)	8	13	20	32	44	63	79	125	
	Surge voltage (SV)										
4.2	Nominal capacitance (Tolerance)	<b>Condition&gt;</b> Measuring F Measuring Vo Measuring T <b>Criteria&gt;</b> Shall be with	requency oltage emperat	: N ure : 20	)±2℃	than 0.5V					
4.3	Leakage current	<b><condition></condition></b> Connecting the capacitor with a protective resistor $(1k\Omega \pm 10\Omega)$ in series for 2 minutes, and then, measure Leakage Current. <b><criteria></criteria></b> Refer to Table 1									
4.4	tan δ	<condition> See 4.2, Norm Capacitance, for measuring frequency, voltage and temperature. <criteria> Refer to Table 1</criteria></condition>									
		Condition> Tensile Street Street the conditions Street	ength of apacitor rength of pacitor,	f, applied f Termina applied f	force to als. orce to b then ber	ent the te	rminal (1	l∼4 mm f original <sub>l</sub>	from the position	rubber) f	
4.5	Terminal	Diamet	er of lea	d wire		ile force (kgf)	IN	Bending (kg			
-	strength		nm and			5 (0.51)		2.5 (	0.25)		
		Over 0.	5mm to	0.8mm	1	0 (1.0)		5 (0	.51)		
		<criteri No notic</criteri 		nanges sh	all be for	ınd, no b	reakage	or loosen	ess at the	e termina	

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		<condition></condition>									
		STEP	STEP Testing Temperature(`		erature(°C)		Time				
		1	20±2		Time	to reach	thermal	equilibrii	ım		
	2		-40(-25)	<u>±3</u>	Time	to reach	thermal	equilibri	ım		
	3		20±2	2	Time	to reach	thermal	equilibrii	ım		
	4	$105\pm 2$			-			•			
	5	$105\pm2$ Time to reach thermal equilibrium $20\pm2$ Time to reach thermal equilibrium									
	<criteria></criteria>				1			- 1			
		a. $\tan \delta$ shall be within the limit of Item 4.4The leakage current measured shall not									
Temperature	more than 8 tin					C					
	Temperature	b. In step 5, ta		-		it of Iter	n 4.4The	leakage	current	shall no	
1.6	characteristi	more than the s									
4.6	cs	c. At-40°C (-2	5°C), ii	mpedance	(z) ratio s	hall not	exceed th	e value	of the fol	lowing	
		table.									
		Working Voltag	ge (V)	6.3	10	16	25	35	50	63	
		Z-25°C/Z+20	)°C	4	3	2	2	2	2	2	
		Z-40°C/Z+20	)°C	8	6	4	3	3	3	3	
		Warking Valtag	ra (M)	100	7						
		Working Voltage Z-25°C/Z+20		2	1						
		Z-40°C/Z+20		3	1						
				_							
			1	< 1000 ···	E AJJO4		41 1000	) E f	7 25/7	20°C	
		For capacitance	e value	> 1000 µ	F, Add 0.5						
					Add 1.0	per ano	ther 1000	μF for.			
		Capacitance, tar			Add 1.0	per ano	ther 1000	μF for.			
		Capacitance, tar	nδ, and	d impedar	Add 1.0 nce shall be	per ano e measur	ther 1000 ed at 120	μF for . )Hz.	Z-40°C/Z	Z+20°C.	
		Capacitance, tar <condition> According to II</condition>	$1 \delta$ , and $0 \delta$	d impedar 34-4No.4.	Add 1.0 nce shall be	per ano e measur s, The ca	ther 1000 ed at 120 apacitor is	μ F for a  OHz.  S stored a	Z-40°C/Z	Z+20°C. erature o	
		Capacitance, tan <condition> According to II  105°C ±2 with</condition>	n δ , and EC6038 n DC bi	d impedar 34-4No.4. as voltage	Add 1.0 nce shall be 13 method e plus the r	per ano e measur s, The ca ated ripp	ther 1000 ed at 120 apacitor is	μ F for . OHz.  s stored at for Tab	Z-40°C/Z at a temp ble 1. (Ti	Z+20°C. erature content of the sum of	
		Capacitance, tar <condition> According to II 105°C ±2 with DC and ripple</condition>	n δ, and EC6038 n DC bi	d impedar 34-4No.4. as voltage voltage sh	Add 1.0 nce shall be 13 method e plus the rhall not ex	per ano e measur s, The ca ated ripp	ther 1000 ed at 120 apacitor is le curren e rated w	F for DHz.  s stored at for Tabyorking	Z-40°C/Z	Z+20°C.  erature of the sum of then the the sum of the the sum of	
	Lord	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should</condition>	EC6038  DC bit peak to be tested	d impedar 34-4No.4. as voltage voltage shed after 16	Add 1.0 nce shall be 13 method e plus the reall not ex 6 hours rec	per ano e measur s, The ca ated ripp	ther 1000 ed at 120 apacitor is le curren e rated w	F for DHz.  s stored at for Tabyorking	Z-40°C/Z	Z+20°C.  erature of the sum of then the the sum of the the sum of	
47	Load	Capacitance, tar <condition> According to II 105°C ±2 with DC and ripple</condition>	EC6038  DC bit peak to be tested	d impedar 34-4No.4. as voltage voltage shed after 16	Add 1.0 nce shall be 13 method e plus the reall not ex 6 hours rec	per ano e measur s, The ca ated ripp	ther 1000 ed at 120 apacitor is le curren e rated w	F for DHz.  s stored at for Tabyorking	Z-40°C/Z	Z+20°C.  erature of the sum of then the the sum of the the sum of	
4.7	life	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m  <criteria></criteria></condition>	EC6038  a DC bit peak be testoreet the	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the reall not ex 6 hours recognized table:	s, The ca ated ripp acceed the	ed at 120 apacitor is le current e rated with the at at	F for DHz.  s stored at for Tabyorking	Z-40°C/Z	Z+20°C.  erature of the sum of then the the sum of the the sum of	
4.7		Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m</condition>	EC6038  DC bi  peak y  be testo eet the	d impedar 34-4No.4. as voltage voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the reall not ex 6 hours recognized table:	per anote measures, The cated ripper acced the overing	ther 1000 ed at 120 apacitor is le curren e rated whime at at ments.	P F for DHz.  S stored at for Tabyorking with mospher	Z-40°C/Z	Z+20°C.  erature of the sum of the the sum of the the sum of the s	
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4.7	life	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m  <criteria> The characteris</criteria></condition>	EC6038  DC bi  peak y  be testueet the	d impedar 34-4No.4. as voltage shed after 16 following	Add 1.0 nce shall be 13 method e plus the reall not ex 6 hours recognized table:  e following Value in the within ±	s, The ca ated ripp acced the overing g require 4.3 shall 25% of	apacitor is le current e rated whime at at ments.  be satisficinitial variation of the current o	DHZ.  S stored at for Tabyorking with mospher iied alue.	z-40°C/z	Z+20°C.  erature of the sum of the the sum of the the sum of the s	
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4.7	life test  Shelf life	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m  <criteria> The characteris  Leakage Capacita tan δ Appeara  <condition> The capacitors a 1000+48/0 hou chamber and b shall be connect applied for 30m</condition></criteria></condition>	EC6038  DC bi  peak be tested be tested be tested be curren ance Ch  mre then ars. Foll e allow cted to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the transpe stored will lowing this yed to stall a series	Add 1.0 nce shall be 13 method e plus the real not ex 6 hours record table:  e following Within ±  Not more There shall the no voltate is period the bilized at a limiting record to the shall be to the shall	g required 4.3 shall 1.25% of than 20. Ill be no ge applicate capacito out the capacito out	ed at 120 apacitor is le current e rated when the satisficial variable leakage of the details at the tors shall apperature to ± 100 Ω.	p F for DHz.  s stored at for Tabyorking mospher  ied falue. e specifie of electron  mperatural be remained for 4~8 ) with I	z-40°C/z  at a temp ble 1. (Ti voltage) ric condit  ad value.  blyte.  re of 105  oved from hours. 1  O.C. rate	±2°C form the test	
	life test	Capacitance, tar  Condition> According to II  105°C ± 2 with DC and ripple product should result should m  Criteria> The characteris  Leakage Capacitatan δ  Appeara  Condition> The capacitors a 1000+48/0 hou chamber and b shall be connected.	EC6038  DC bi  peak be tested be tested be tested be curren ance Ch  mre then ars. Foll e allow cted to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the transpe stored will lowing this yed to stall a series	Add 1.0 nce shall be 13 method e plus the real not ex 6 hours record table:  e following Within ±  Not more There shall the no voltate is period the bilized at a limiting record to the shall be to the shall	g required 4.3 shall 1.25% of than 20. Ill be no ge applicate capacito out the capacito out	ed at 120 apacitor is le current e rated when the satisficial variable leakage of the details at the tors shall apperature to ± 100 Ω.	p F for DHz.  s stored at for Tabyorking mospher  ied falue. e specifie of electron  mperatural be remained for 4~8 ) with I	z-40°C/z  at a temp ble 1. (Ti voltage) ric condit  ad value.  blyte.  re of 105  oved from hours. 1  O.C. rate	±2°C form the test Next the d voltage	
	life test  Shelf life	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m  <criteria> The characteris  Leakage Capacita tan δ Appeara  <condition> The capacitors a 1000+48/0 hou chamber and b shall be connect applied for 30m</condition></criteria></condition>	EC6038  DC bi  peak be tested be tested be tested be curren ance Ch  mre then ars. Foll e allow cted to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the transpe stored will lowing this yed to stall a series	Add 1.0 nce shall be 13 method e plus the real not ex 6 hours record table:  e following Within ±  Not more There shall the no voltate is period the bilized at a limiting record to the shall be to the shall	g required 4.3 shall 1.25% of than 20. Ill be no ge applicate capacito out the capacito out	ed at 120 apacitor is le current e rated when the satisficial variable leakage of the details at the tors shall apperature to ± 100 Ω.	p F for DHz.  s stored at for Tabyorking mospher  ied falue. e specifie of electron  mperatural be remained for 4~8 ) with I	z-40°C/z  at a temp ble 1. (Ti voltage) ric condit  ad value.  blyte.  re of 105  oved from hours. 1  O.C. rate	±2°C form the test Next the d voltage	
	life test  Shelf life	Capacitance, tar <condition> According to II  105°C ±2 with DC and ripple product should result should m  <criteria> The characteris  Leakage Capacita tan δ Appeara  <condition> The capacitors a 1000+48/0 hou chamber and b shall be connect applied for 30m</condition></criteria></condition>	EC6038  DC bi  peak be tested be tested be tested be curren ance Ch  mre then ars. Foll e allow cted to	d impedar 34-4No.4. as voltage shed after 16 following Ill meet the transpe stored will lowing this yed to stall a series	Add 1.0 nce shall be 13 method e plus the real not ex 6 hours record table:  e following Within ±  Not more There shall the no voltate is period the bilized at a limiting record to the shall be to the shall	g required 4.3 shall 1.25% of than 20. Ill be no ge applicate capacito out the capacito out	ed at 120 apacitor is le current e rated when the satisficial variable leakage of the details at the tors shall apperature to ± 100 Ω.	p F for DHz.  s stored at for Tabyorking mospher  ied falue. e specifie of electron  mperatural be remained for 4~8 ) with I	z-40°C/z  at a temp ble 1. (Ti voltage) ric condit  ad value.  blyte.  re of 105  oved from hours. 1  O.C. rate	±2°C form the test Next the d voltage	

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ı			
		<criteria></criteria>	1 0 11
		The characteristic shall meet t	
	Shelf	Leakage current	Value in 4.3 shall be satisfied
4.8	life	Capacitance Change	Within $\pm 25\%$ of initial value.
4.0	test	tan δ	Not more than 200% of the specified value.
	test	Appearance	There shall be no leakage of electrolyte.
			stored more than 1 year, the leakage current may
		increase. Please apply voltage	e through about 1 k $\Omega$ resistor, if necessary.
		<condition></condition>	
			e capacitor connected with a $(100\pm50)/C_R(k\Omega)$ resistor
			ted to 1000 cycles, each consisting of charge of $30 \pm 5$ s
		followed discharge of 5 min 3	
		The test temperature shall be	
		C <sub>R</sub> : Nominal Capacitance ( )	1 F)
4.0	Surge	<criteria></criteria>	N. d. d. 'C' 1 1
4.9	test	Leakage current	Not more than the specified value.
		Capacitance Change	Within $\pm 15\%$ of initial value.
		tan δ	Not more than the specified value.
		Appearance	There shall be no leakage of electrolyte.
		Attention:	
			ge at abnormal situation only. It is not applicable to such
		over voltage as often applied.	•
		perpendicular directions.  Vibration frequency rate Peak to peak amplitude Sweep rate Mounting method: The capacitor with diameter g in place with a bracket.  4mm or less	: 1.5mm : 10Hz ~ 55Hz ~ 10Hz in about 1 minute reater than 12.5mm or longer than 25mm must be fixed Within 30°
4.10	Vibration test		
		<criteria> After the test, the following in</criteria>	To be soldered  tems shall be tested:  No intermittent contacts, open or short circuiting.
			No damage of tab terminals or electrodes.
			No mechanical damage in terminal. No leakage
		Appearance	of electrolyte or swelling of the case.  The markings shall be legible.

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		<condition></condition>					
		The capacitor shall be tes		_	onditions	s:	
		Soldering temperature		245±3°C			
	0.111.77	Dipping depth		2mm			
4.11	Solderability	Dipping speed		25±2.5mm/s	3		
	test	Dipping time	:	$3\pm0.5s$			
		<criteria></criteria>		Aii	-£050/ ·	- C41C 1	
		Coating quality  A minimum of immersed			01 95% (	or the surface (	being
		<condition></condition>					
		Terminals of the capacito	r shall be im	mersed into	solder b	bath at $260\pm 3$	5°Cfor10±
		1seconds or $400 \pm 10^{\circ}\text{C}$ for	or3 <sup>+1</sup> <sub>-0</sub> seconds	s to 1.5~2.0m	nm from	the body of ca	apacitor.
		Then the capacitor shall be					
	Resistance to	for 1~2 hours before mea			r		
4.12	solder heat	<c<u>riteria&gt;</c<u>					
	test	Leakage current	Not	more than the	e specifi	ed value.	
		Capacitance Change	With	$\sin \pm 10\%$ of	`initial v	value.	
		tan 8	Not	more than the	e specifi	ed value.	
		Appearance	Ther	There shall be no leakage of electrolyte.			
		C . 1'4'					
		<condition> Temperature Cycle:Acco</condition>	rding to IEC	50384-4No.4	7metho	de canacitor e	hall he
		placed in an oven, the con				us, capacitor s	man oc
			emperature			ime	
		(1)+20°C			€3	Minutes	
		(2)Rated low temper	-	$30\pm 2$	Minutes		
4.12	Change of	(3)Rated high tempe		-	$\frac{30\pm 2}{30\pm 2}$	Minutes	
4.13	temperature test			C)	<u> </u>	Williates	
	iest	(1) to (3)=1 cycle, to <b>Criteria&gt;</b>	ital 3 cycle				
		The characteristic shall m	neet the follow	ving requiren	ment		
		Leakage current				zalue	
		tan 8		e than the sp			
		Appearance		nall be no leal			
		<condition></condition>	THERE SI	iun oc no icu	Ruge or	creetiory te.	
		Humidity Test:					
		According to IEC60384-	4No 4 12 met	thods capacit	tor shall	be exposed for	or 500±8
		hours in an atmosphere o		_		-	
		meet the following requir		1.41 10_2 0	, 1110 011		ange snan
		<criteria></criteria>					
4 1 4	Damp heat	Leakage current	Not more the	han the speci	fied valu	ie.	
4.14	test	Capacitance Change	Within ±2	0% of initial	l value.		
		tan δ	Not more th	han 120% of	the spec	cified value.	
		Appearance	There shall	be no leakag	ge of ele	ctrolyte.	

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4.15	Vent test	22.4 or less	rrent (A) 1 10 dangero	plarity revolution	versed to	a DC po	wer source	. Then a
	Maximum permissible	Condition> The maximum permissible ri at 120Hz and can be applied Table-1 The combined value of D.C rated voltage and shall not r Frequency Multipliers: Coefficient	l at maxi voltage	mum ope	erating te	mperatur	re	ceed the
4.16	(ripple current)	Cap. (µF)	0.45	0.55	0.70	0.90	1.00	
		39~330	0.60	0.70	0.85	0.95	1.00	
		390~1000	0.65	0.75	0.90	0.98	1.00	
		1200~3900	0.75	0.80	0.95	1.00	1.00	

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5. It refers to the latest document of "Environment-related Substances standard" (WI-HSPM-QA-072).

	Substances				
	Cadmium and cadmium compounds				
Heavy metals	Lead and lead compounds				
	Mercury and mercury compounds				
	Hexavalent chromium compounds				
	Polychlorinated biphenyls (PCB)				
Chloinated	Polychlorinated naphthalenes (PCN)				
organic	Polychlorinated terphenyls (PCT)				
compounds	Short-chain chlorinated paraffins(SCCP)				
	Other chlorinated organic compounds				
D : 1	Polybrominated biphenyls (PBB)				
Brominated	Polybrominated diphenylethers(PBDE) (including				
organic	decabromodiphenyl ether[DecaBDE])				
compounds	Other brominated organic compounds				
Tributyltin compo	ounds(TBT)				
Triphenyltin com	pounds(TPT)				
Asbestos					
Specific azo com	pounds				
Formaldehyde					
Beryllium oxide					
Beryllium coppe	er				
Specific phthalate	es (DEHP,DBP,BBP,DINP,DIDP,DNOP,DNHP)				
Hydrofluorocarbo	on (HFC), Perfluorocarbon (PFC)				
Perfluorooctane s	ulfonates (PFOS)				
Specific Benzotri	azole				

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### ELECTROLYTIC CAPACITOR SPECIFICATION GT SERIES

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#### **Attachment: Application Guidelines**

#### 1.Circuit Design

#### 1.1 Operating Temperature and Frequency

Electrolytic capacitor electrical parameters are normally specified at 20°C temperature and 120Hz frequency. These parameters vary with changes in temperature and frequency. Circuit designers should take these changes into consideration.

- (1) Effects of operating temperature on electrical parameters
  - a) At higher temperatures, leakage current and capacitance increase while equivalent series resistance (ESR) decreases.
  - b) At lower temperatures, leakage current and capacitance decrease while equivalent series resistance (ESR) increases.
- (2) Effects of frequency on electrical parameters
  - a) At higher frequencies capacitance and impedance decrease while tanδ increases.
  - b) At lower frequencies, ripple current generated heat will rise due to an increase in equivalent series resistance (ESR).

#### 1.2 Operating Temperature and Life Expectancy

See the file: Life calculation of aluminum electrolytic capacitor

#### 1.3 Common Application Conditions to Avoid

The following misapplication load conditions will cause rapid deterioration to capacitor electrical parameters. In addition, rapid heating and gas generation within the capacitor can occur causing the pressure relief vent to operate and resultant leakage of electrolyte. Under Leaking electrolyte is combustible and electrically conductive.

#### (1) Reverse Voltage

DC capacitors have polarity. Verify correct polarity before insertion. For circuits with changing or uncertain polarity, use DC bipolar capacitors. DC bipolar capacitors are not suitable for use in AC circuits.

#### (2) Charge / Discharge Applications

Standard capacitors are not suitable for use in repeating charge / discharge applications. For charge / discharge applications consult us and advise actual conditions.

#### (3) Over voltage

Do not apply voltages exceeding the maximum specified rated voltage. Voltages up to the surge voltage rating are acceptable for short periods of time. Ensure that the sum of the DC voltage and the superimposed AC ripple voltage does not exceed the rated voltage.

#### (4) Ripple Current

Do not apply ripple currents exceeding the maximum specified value. For high ripple current applications, use a capacitor designed for high ripple currents or contact us with your requirements. Ensure that allowable ripple currents superimposed on low DC bias voltages do not cause reverse voltage conditions.

#### 1.4 Using Two or More Capacitors in Series or Parallel

#### (1) Capacitors Connected in Parallel

The circuit resistance can closely approximate the series resistance of the capacitor causing an imbalance of ripple current loads within the capacitors. Careful design of wiring methods can minimize the possibility of excessive ripple currents applied to a capacitor.

#### (2) Capacitors Connected in Series

Normal DC leakage current differences among capacitors can cause voltage imbalances. The use of voltage divider shunt resistors with consideration to leakage current can prevent capacitor voltage imbalances.

#### 1.5 Capacitor Mounting Considerations

#### (1) Double Sided Circuit Boards

Avoid wiring pattern runs, which pass between the mounted capacitor and the circuit board.

When dipping into a solder bath, excess solder may collect under the capacitor by capillary action and short circuit the anode and cathode terminals.

#### (2) Circuit Board Hole Positioning

The vinyl sleeve of the capacitor can be damaged if solder passes through a lead hole for subsequently processed parts. Special care when locating hole positions in proximity to capacitors is recommended.

#### (3)Circuit Board Hole Spacing

The circuit board holes spacing should match the capacitor lead wire spacing within the specified tolerances. Incorrect spacing can cause excessive lead wire stress during the insertion process. This may result in premature capacitor failure due to short or open circuit, increased leakage current, or electrolyte leakage.

#### (4) Clearance for Case Mounted Pressure Relief vents

Capacitors with case mounted pressure relief vents require sufficient clearance to allow for proper vent operation. The minimum clearances are dependent on capacitor diameters as proper vent operation. The minimum clearances are dependent on capacitor diameters as follows.

φ6.3~φ16mm:2mm minimum, φ18~φ35mm:3mm minimum, φ40mm or greater:5mm minimum.

#### (5) Clearance for Seal Mounted Pressure Relief Vents

A hole in the circuit board directly under the seal vent location is required to allow proper release of pressure.

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#### (6) Wiring Near the Pressure Relief Vent

Avoid locating high voltage or high current wiring or circuit board paths above the pressure relief vent. Flammable, high temperature gas exceeding 100°C may be released which could dissolve the wire insulation and ignite.

(7) Circuit Board patterns Under the Capacitor

Avoid circuit board runs under the capacitor as electrolyte leakage could cause an electrical short.

(8) Screw Terminal Capacitor Mounting

Do not orient the capacitor with the screw terminal side of the capacitor facing downwards.

Tighten the terminal and mounting bracket screws within the torque range specified in the specification.

#### 1.6 Electrical Isolation of the Capacitor

Completely isolate the capacitor as follows.

- (1) Between the cathode and the case (except for axially leaded B types) and between the anode terminal and other circuit paths
- (2) Between the extra mounting terminals (on T types) and the anode terminal, cathode terminal, and other circuit paths.
- 1.7 The Product endurance should take the sample as the standard.
- 1.8 If conduct the load or shelf life test, must be collect date code within 6 months products of sampling.

#### 1.9 Capacitor Sleeve

The vinyl sleeve or laminate coating is intended for marking and identification purposes and is not meant to electrically insulate the capacitor.

The sleeve may split or crack if immersed into solvents such as toluene or xylene, and then exposed to high temperatures.

#### CAUTION!

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- (1) Provide protection circuits and protection devices to allow safe failure modes.
- (2) Design redundant or secondary circuits where possible to assure continued operation in case of main circuit failure.

#### 2. Capacitor Handling Techniques

- 2.1 Considerations Before Using
- (1) Capacitors have a finite life. Do not reuse or recycle capacitors from used equipment.
- (2) Transient recovery voltage may be generated in the capacitor due to dielectric absorption. If required, this voltage can be discharged with a resistor with a value of about 1kΩ.
- (3) Capacitors stored for long periods of time may exhibit an increase in leakage current. This can be corrected by gradually applying rated voltage in series with a resistor of approximately  $1k\Omega$ .
- (4) If capacitors are dropped, they can be damaged mechanically or electrically. Avoid using dropped capacitors.
- (5) Dented or crushed capacitors should not be used. The seal integrity can be compromised and loss of electrolyte / shortened life can result.

#### 2.2 Capacitor Insertion

- (1) Verify the correct capacitance and rated voltage of the capacitor.
- (2) Verify the correct polarity of the capacitor before inserting.
- (3) Verify the correct hole spacing before insertion (land pattern size on chip type) to avoid stress on the terminals.
- (4) Ensure that the auto insertion equipment lead clinching operation does not stress the capacitor leads where they enter the seal of the capacitor.

For chip type capacitors, excessive mounting pressure can cause high leakage current, short circuit, or disconnection.

#### 2.3 Manual Soldering

- (1) Observe temperature and time soldering specifications or do not exceed temperatures of 400 ℃ for 3 seconds or less.
- (2) If lead wires must be formed to meet terminal board hole spacing, avoid stress on the lead wire where it enters the capacitor seal.
- (3) If a soldered capacitor must be removed and reinserted, avoid excessive stress to the capacitor leads.
- (4) Avoid touching the tip of the soldering iron to the capacitor, to prevent melting of the vinyl sleeve.

#### 2.4 Flow Soldering

- (1) Do not immerse the capacitor body into the solder bath as excessive internal pressure could result.
- (2) Observe proper soldering conditions (temperature, time, etc.) Do not exceed the specified limits.
- (3) Do not allow other parts or components to touch the capacitor during soldering.

#### 2.5 Other Soldering Considerations

Rapid temperature rises during the preheat operation and resin bonding operation can cause cracking of the capacitor vinyl sleeve. For heat curing, do not exceed 150°C for a maximum time of 2 minutes.

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- 2.6 Capacitor Handling after Solder
- (1). Avoid movement of the capacitor after soldering to prevent excessive stress on the lead wires where they enter the seal.
- (2). Do not use capacitor as a handle when moving the circuit board assembly.
- (3). Avoid striking the capacitor after assembly to prevent failure due to excessive shock.

#### 2.7 Circuit Board Cleaning

- (1) Circuit boards can be immersed or ultrasonically cleaned using suitable cleaning solvents for up 5 minutes and up to 60°C maximum temperatures. The boards should be thoroughly rinsed and dried. The use of ozone depleting cleaning agents is not recommended in the interest of protecting the environment.
- (2) Avoid using the following solvent groups unless specifically allowed for in the specification;

Halogenated cleaning solvents: except for solvent resistant capacitor types, halogenated solvents can permeate the seal and cause internal capacitor corrosion and failure. For solvent resistant capacitors, carefully follow the temperature and time requirements of the specification. 1-1-1 trichloroethane should never be used on any aluminum electrolytic capacitor.

Alkali solvents : could attack and dissolve the aluminum case.

Petroleum based solvents: deterioration of the rubber seal could result.

Xylene : deterioration of the rubber seal could result.

Acetone : removal of the ink markings on the vinyl sleeve could result.

- (3) A thorough drying after cleaning is required to remove residual cleaning solvents which may be trapped between the capacitor and the circuit board. Avoid drying temperatures, which exceed the maximum rated temperature of the capacitor.
- (4) Monitor the contamination levels of the cleaning solvents during use by electrical conductivity, pH, specific gravity, or water content. Chlorine levels can rise with contamination and adversely affect the performance of the capacitor. Please consult us for additional information about acceptable cleaning solvents or cleaning methods.

#### 2.8 Mounting Adhesives and Coating Agents

When using mounting adhesives or coating agents to control humidity, avoid using materials containing halogenated solvents. Also, avoid the use of chloroprene based polymers. After applying adhesives or coatings, dry thoroughly to prevent residual solvents from being trapped between the capacitor and the circuit board.

#### 3. Precautions for using capacitors

3.1 Environmental Conditions

Capacitors should not be stored or used in the following environments.

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

#### 3.2 Electrical Precautions

- (1) Avoid touching the terminals of the capacitor as possible electric shock could result. The exposed aluminum case is not insulated and could also cause electric shock if touched.
- (2) Avoid short circuit the area between the capacitor terminals with conductive materials including liquids such as acids or alkaline solutions.

#### 4. Emergency Procedures

- (1) If the pressure relief vent of the capacitor operates, immediately turn off the equipment and disconnect form the power source. This will minimize additional damage caused by the vaporizing electrolyte.

If electrolyte or gas enters the eye, immediately flush the eyes with large amounts of water.

If electrolyte or gas is ingested by month, gargle with water.

If electrolyte contacts the skin, wash with soap and water.

#### 5. Long Term Storage

Leakage current of a capacitor increases with long storage times. The aluminum oxide film deteriorates as a function of temperature and time. If used without reconditioning, an abnormally high current will be required to restore the oxide film. This current surge could cause the circuit or the capacitor to fail. After one year, a capacitor should be reconditioned by applying rated voltage in series with a  $1000\Omega$ , current limiting resistor for a time period of 30 minutes . If the expired date of products date code is over eighteen months, the products should be return to confirmation.

#### 5.1 Environmental Conditions

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The capacitor shall be not use in the following condition:

- (1) Temperature exposure above the maximum rated or below the minimum rated temperature of the capacitor.
- (2) Direct contact with water, salt water, or oil.
- (3) High humidity conditions where water could condense on the capacitor.
- (4) Exposure to toxic gases such as hydrogen sulfide, sulfuric acid, nitric acid, chlorine, or ammonia.
- (5) Exposure to ozone, radiation, or ultraviolet rays.
- (6) Vibration and shock conditions exceeding specified requirements.

#### 6. Capacitor Disposal

When disposing of capacitors, use one of the following methods.

Incinerate after crushing the capacitor or puncturing the can wall (to prevent explosion due to internal pressure rise). Capacitors should be incinerated at high temperatures to prevent the release of toxic gases such as chlorine from the polyvinyl chloride sleeve, etc.

Dispose of as solid waste.

NOTE: Local laws may have specific disposal requirements, which must be followed.

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